## Full Length Research Paper

# Simulation analysis on high step-up DC-DC converter for fuel cell system

E. Narendran, S. Harikrishnan\*, K. C. Ramya Ashokkumar and R. Malathi

Department of Electrical and Electronics Engineering, Rajiv Gandhi College of Engineering, Sriperumbudur, Chennai, India.

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This paper presents a high step-up DC-DC converter for fuel cell system. The proposed converter comprises of an input-current doubler, an output-voltage doubler, and an active-clamp circuit. The input-current doubler and the output-voltage doubler both provide much higher voltage conversion ratio by using a low turn ratio transformer. The low turns ratio increases the overall efficiency. A series resonant circuit of the output voltage doubler removes the reverse recovery problem of the rectifying diodes. The active-clamp circuit clamps the surge voltage of switches and recycles the energy stored in the leakage inductance of the transformer. The proposed converter has been simulated in OrCAD software. Simulation results proved that based on the transformer design, better performance of the converter can be achieved for any value of input voltage.

**Key words:** Series resonant circuit, active clamp circuit, input-current doubler, output-voltage doubler.

#### INTRODUCTION

A step-up dc-dc converter comprises a direct current power source, an inductor, a first switching element, a second switching element, a smoothing capacitor, a driver controller for switching ON or OFF the first switching element and the second switching element, and a control changing unit for changing a control operation of the driver controller according to a load current. A frequently used method for a power device to convert an input voltage to a desired output voltage includes power conversion at high efficiency by using a switching dc-dc converter. In multiport dc-dc converters, an effective duty ratio control method was implemented to extend the ZVS operating range when input voltages varied widely. Softswitching conditions over the full operating range are achievable by adjusting the duty ratio of the voltage applied to the transformer winding in response to the dc voltage variations at the port (Haimin et al., 2008).

A new dc-dc converter for solid oxide fuel cell powered auxiliary power unit was developed. Here, the converter did not consider the leakage inductance of the transformer as a parasite and used it for energy transfer, thereby

increasing the efficiency of the converter (Sharma and Hongwei, 2006). A new approach was developed in one research paper for analyzing the V-I characteristics of fuel cells stacks based on DC-DC converters. The measurement of the desired characteristic of fuel cells included high speed of response, high fidelity and low cost. The obtained results had shown that all dc-dc topologies were not capable of reproducing this characteristic (Duran et al., 2008). Another strategy was developed for an isolated boost full bridge converter to employ soft-commutating method and control scheme. Zero-voltage-switching (ZVS) was achieved for all the switches at the voltage-fed side inverter in boost mode operation (Zhu, 2006).

A single-stage current-fed full-bridge boost converter with power factor correction (PFC) and zero current switching (ZCS) for high voltage application was studied. The variable frequency control scheme with ZCS was employed to regulate the output voltage and achieve high power factor. The maximum power factor and efficiency obtained were 0.995 and 87.4 at full load condition with an input voltage of 220V (Chen et al., 2008). A new active clamping ZVS pulse width modulation (PWM) current-fed half-bridge converter was designed and analyzed to ensure the higher operating frequency, smaller-sized reactive components, lower cost of production

<sup>\*</sup>Corresponding author. E-mail: ramhkn@yahoo.co.in. Tel: 009894037206.

easier implementation, and higher efficiency (Han et al., 2005). A dual series-resonant active-clamp dc-dc converter was presented to obtain high efficiency. This converter used an active-clamp technique which served to recycle the energy stored in the leakage inductance and provided zero-voltage turn-on switching. The voltage stresses of the main switch were clamped (Lee et al., 2008). A novel two-stage step-up dc-dc converter was developed to increase the voltage from 24 V to 270 V for electric vehicle power system application. This device had the advantages of high power density, easy control, high efficiency, high transformer utilization, and fast dynamic response (Du et al., 2007).

To overcome limited voltage step-up ratio in a classical boost converter, non-isolated high step-up technique based on isolated type converter was introduced. It was tested experimentally and the results showed the better performance (Park et al., 2009). Consider a high efficiency step-up converter with low switch voltage stress for fuel cell system applications in which input and output ripples were reduced and also higher voltage conversion ratio was achieved (Pan and Lai, 2009). A novel high step-up dc-dc converter was introduced for flow-input-voltage fuel cell energy conversion applications. Here, a multi-windings coupled inductor and a voltage doubler were employed for achieving high step-up voltage gain (Changchien et al., 2009).

High step-up converter with coupled-inductor was experimentally studied as another strategy in which the closed-loop control methodology was introduced in the proposed scheme to overcome the voltage drift problem of the power source under the load variations. The result was that the proposed converter topology enhanced the voltage gain of a conventional boost converter with a single inductor, and dealt with the problem of the leakage inductor and demagnetization of transformer for a coupled-inductor-based converter (Wai and Duan, 2005). A forward energy-delivered technique was presented to reduce the power loss in the step-up ZVS full-bridge converter. The forward energy-delivered circuit (FEDC) closely wound in the resonant inductor was able to alleviate the high ringing ripples incorporating in the primary current and delivered excess energy to the load during step-up conversion (Hsieh and Yang, 2008). Hence, a high step up dc-dc converter with high voltage conversion ratio, low input current ripple and high efficiency is required.

#### **CONVENTIONAL METHOD**

A conventional current-fed half-bridge converter has the input-current doubler function. Since the input current divides into two inductor currents, reducing the primary current of the transformer, the related conduction loss is reduced. However, this converter has several problems such as voltage spikes of switches resulting from the

leakage inductance of the transformer, high-voltage stress of the rectifying diodes, and low power conversion efficiency.

### Need for step-up DC-DC converter

Generally, the fuel cell stack has a low voltage and its current ripple must be small. Therefore, a high step-up dc-dc converter with high voltage conversion ratio, low input-current ripple, and high efficiency is required.

#### DC - DC converter

DC to DC converter is an electronic circuit which converts a source of direct current (DC) from one voltage level to another. Every electronic system is designed to operate from a supply voltage, which is usually assumed to be constant. A voltage regulator provides this constant DC output voltage and contains circuitry that continuously holds the output voltage at the design value regardless of changes in load current or input voltage, assuming that the load current and input voltage are within the specified operating range for that regulator.

In portable systems, the input voltage is often obtained from a battery. DC to DC converter type regulators receive such fixed DC voltage as input and produce the desired voltage as output which could be higher or lower than the input voltage. DC to DC converter accepts a fixed DC input voltage and produces a DC output voltage at a different level than the input. They are also used to provide noise isolation, power bus regulation, etc.

#### Types of DC-DC converter

DC to DC converters can be classified broadly into two categories:

- 1. Isolated DC-DC converter
- 2. Non-isolated DC-DC converter

This "isolation" refers to the existence of an electrical barrier between the input and output of the DC-DC converter. An isolated DC-DC converter will have a high frequency transformer providing that barrier. This barrier can withstand anything from a few hundred volts to several thousand volts, as is required for medical application. Another feature of an isolated converter is that the output can be configured to be either positive or negative.

As in the case of non-isolated DC-DC converter, it has one major drawback. In spite its low cost and simplicity, it reveals that there is an electrical connection between the input and output. Many safety agency bodies and/or customers require a separation from the input and output

voltage which is often user accessible.

#### PROPOSED METHOD

Active clamping current-fed half-bridge converters have been presented to solve the above mentioned problems. Its activeclamping circuit clamps the surge voltage of switches and recycles the energy stored in the leakage inductance. However, the reverserecovery problem of the output rectifying diodes still exists. In the current project a high-efficiency high step-up dc-dc converter is proposed for fuel cell power systems. The proposed system consists of an input-current doubler, an output-voltage doubler, and an active-clamp circuit. The input-current doubler and the outputvoltage doubler provide a higher voltage conversion ratio without using a high turns ratio in the transformer and increase the overall efficiency. A series-resonant circuit of the output-voltage doubler removes the reverse-recovery problem of the rectifying diodes. The proposed dc-dc converter has low conduction loss in the primary side, since the input-current doubler divides the input current into two inductor currents, reducing the primary current of the transformer, and the primary switches with the active-clamp circuit have low ON-resistance by reducing voltage stress.

The input-current doubler and the output-voltage doubler provide a higher voltage conversion ratio without using a high turns-ratio transformer while increasing the overall efficiency. The output-voltage doubler doubles the conversion ratio and the voltage stress on rectifying diodes is confined to the output voltage. This voltage doubler provides a series-resonant circuit that consists of a transformer leakage inductance and a resonant capacitor. Then, the zero-current turn-OFF of the output rectifying diodes is achieved, and the reverse-recovery losses are eliminated.

#### Voltage-fed converters

Voltage-fed converters are often used in the step-up dc-dc converter for fuel cells. However, these voltage-fed converters may not be optimal due to large input-current ripple. Reducing the input-current ripple in the voltage-fed converter requires an additional LC filter across the fuel cell stack, which lowers the power efficiency. Also, a high turns ratio between the primary and secondary sides of the high-frequency transformer is required.

#### **Current-fed converters**

Current-fed converters decrease the input-current ripple by using an inductor. Current-fed converters have advantages of high voltage conversion ratio, low input-current ripple, and low conduction loss of switches. Several current-fed converters have been developed such as a current-fed push—pull converter, a current-fed full-bridge converter, and a current-fed half-bridge converter.

#### **SIMULATION**

Simulation has become a very powerful tool in industrial applications as well as in academics, nowadays. It is now essential for an electrical engineer to understand the concept of simulation and learn its use in various applications. Simulation is one of the best ways to study the system or circuit behavior without damaging it. The tools for doing the simulation in various fields are available in the market for engineering professionals.

Many industries are spending a considerable amount of time and money in doing simulation before manufacturing their product. In most research and development (R and D) work, simulation plays a

very important role. Without simulation, it is quite impossible to proceed further. Electronic circuit design requires accurate methods for evaluating the performance of the circuit. Because of the enormous complexity of modern integrated circuits, computer-aided circuit analysis is essential and can provide information about circuit performance that is almost impossible to obtain with laboratory prototype measurements.

SPICE is a general-purpose circuit program that simulates electronic circuits. SPICE can perform various analyses of electronic circuits. SPICE contains models for common circuit elements, active as well as passive, and is widely used both in industries and universities.

The acronym SPICE stands for Simulated Program of Integrated Circuit Emphasis. Till now, PSPICE is available only on the main frame computers. In addition, the initial cost of the mainframe computer is expensive and inconvenient for classroom use. SPICE, therefore, widens the scope for the integration of computer-aided analysis into electronic circuit courses at the undergraduate level. In 1984, Microsim introduced the PSPICE simulator, which is similar to the Berkeley SPICE and runs on an IBM-PC or compatible. The other versions of SPICE that will run on computers such as the Macintosh 2, V AX, SUN, and NEC are also available.

#### Types of analysis

PSPICE allows various types of analysis. Each analysis is invoked by including its command statement. For example, a statement beginning with the DC command invokes the DC sweep. The types of analysis and their corresponding (Dot) commands are described below.

DC analysis is used for circuits with time-invariant sources. It calculates all node voltages and branch

currents over a range of values, and their quiescent (dc) values are at the outputs. DC sweep of an input voltage/current source, a model parameter, or temperature over a wide range of values, (DC) determination of the liberalized model parameters of non linear devices, (.OP) DC operating point to obtain all node voltages (.OP) small-signal transfer function with small-signal gain, input resistance, and output resistance small-sign transient analysis is used for circuits with time-variant sources. It calculates all sensitivities (.SENS).

All node voltages and branch currents over a time interval, and their instantaneous values are the outputs. Circuit behavior in response to time varying sources (.TRAN) DC and Fourier components of the transient analysis results (.FOUR) AC analysis is used for small-signal analysis of circuits with sources of variable frequencies. It calculates all node voltages and branch currents over a range of frequencies, and their magnitudes and phase angles are the outputs.

#### Limitations of PSPICE

As a circuit simulator PSPICE has the following limitations:

- 1. The program is not interactive, that is the circuit cannot be analyzed for various component values without editing the program statements.
- PSPICE does not support an iterative method of solution. If the elements of a circuit are specified, PSPICE cannot be used to synthesize the circuit elements.
- 3. The PC version needs 512 MB of memory (RAM) to run.
- 4. Distortion analysis is not available in PSPICE. SPICE 2 allows distortion analysis, but gives wrong answers.
- 5. The output impedance of a circuit cannot be printed or plotted.

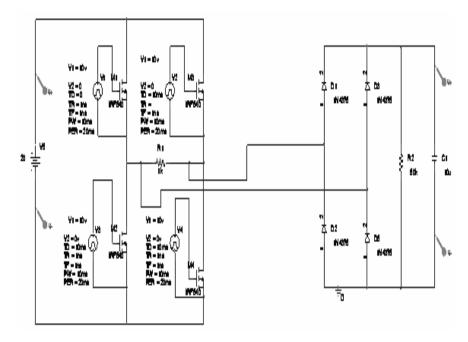


Figure 1. Non-isolated DC-DC converter.

#### Simulation tools

Simulation of power electronic converters can be done, either by (I) circuit oriented simulator (e.g. PSPICE, PSIM, TINA, electronics workbench, etc), or by (2) mathematical model based equation solvers using high level languages (e.g., C++, VISUALBASIC, etc.), or high level language based packages (MATLAB, MATHEMATICA, etc). In this we can use the circuit-oriented simulators; Also few software simulation tools are listed which are widely accepted by the engineering society, particularly by the electrical engineers. The simulation tools are used in industries as well as in academic institutions.

#### **RESULTS AND DISCUSSION**

Figure 1 illustrates the circuit of non-isolated DC-DC converter in which there is single phase power MOSFET based full bridge inverter as input section and single phase bridge rectifier as output section. The output voltage waveform of the non-isolated DC-DC converter is seen in Figure 2. It is seen that for the input voltage of 20 V, output obtained the value below 20 V. It is observed that the output voltage is less than the applied input voltage. This is due to the ON state resistance incurred by the power MOSFETs.

Figure 3 illustrates the circuit of isolated DC-DC converter consisting of single phase power MOSFET based full bridge inverter as input section, single phase bridge rectifier as output section and a high frequency transformer used as an isolator. Figure 4 represents that when the input voltage was given as 20 V, the output voltage obtained was below 20 V in this isolated DC-DC converter. Due to existence of conduction losses in the power MOSFET, output voltage is less than that of input

voltage.

Figure 5 presents the circuit of current doubler circuit including single phase power MOSFET based full bridge inverter, inductors  $L_1$  and  $L_2$ , and a high frequency transformer. Figure 6 represents the response of current doubler in which the output current had been doubled than that of the input current. The reason for this is that resonant components were connected in series with the inverter.

Figure 7 shows the voltage doubler circuit comprising of single phase power MOSFET based full bridge inverter as input section, rectifier as output section and a high frequency transformer used as an isolator. Figure 8 shows the response of voltage doubler in which the output voltage had been doubled as 60 V for the given input of 30 V. This is due to the presence of full wave rectifier with filter in the secondary side of the transformer which in turn acts as a voltage doubler circuit.

# Performance comparison between conventional converter and proposed converter

Figure 9 depicts the comparison between the conventional and proposed converters in respect of their performance. When the input voltage was given as 40 V to both converters, outputs were obtained as 35 and 80 V in conventional converter and proposed converter respectively, shown in Figure 9. Thus, it clearly suggests that the proposed converter achieved greater performance rather than the conventional performance for the same input voltage.

The voltage spikes, switching losses and leakage losses

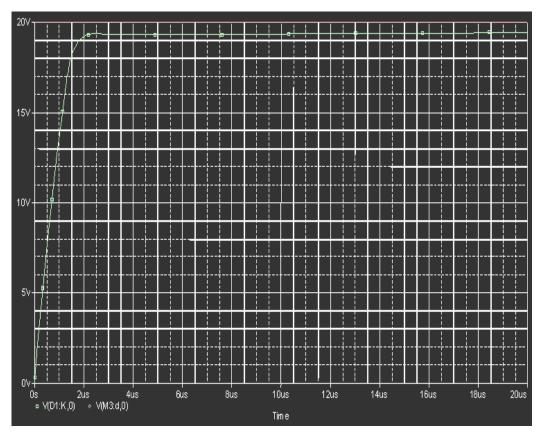


Figure 2. Output voltage waveform of non-isolated DC/DC converter.

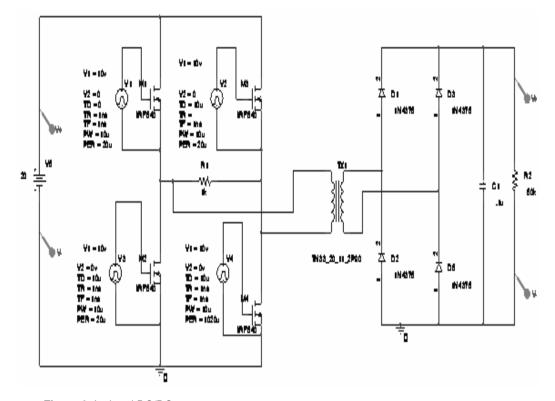


Figure 3. Isolated DC/DC converter.

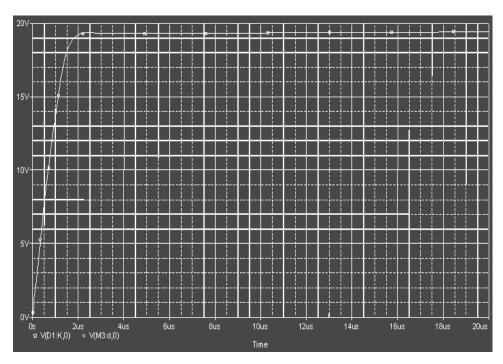


Figure 4. Output voltage waveform of isolated DC/DC converter.

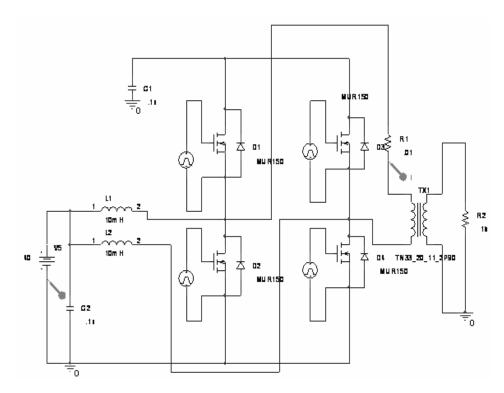


Figure 5. Current doubler circuit.

were eliminated by the active clamping circuit in the proposed converter. This active clamp circuits are used to clamp the voltage spikes of the switch and to recycle the energy in the leakage inductance. As a result, the

voltage stress on the secondary side rectifiers was reduced significantly. These enhancements allow DC-DC topologies with an input-current doubler and output-voltage doubler to operate efficiently and effectively at

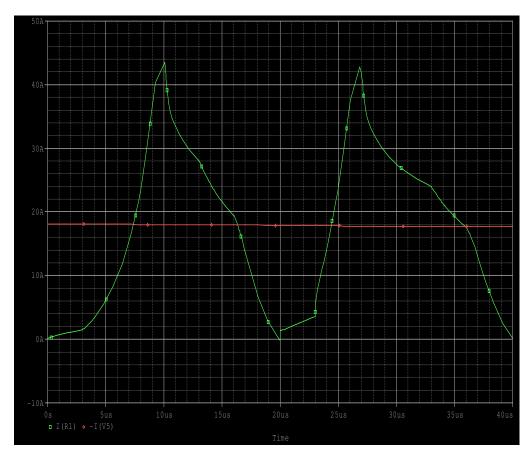


Figure 6. Input current waveform of current doubler.

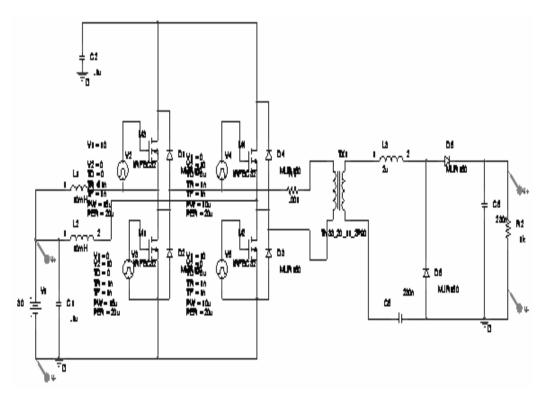


Figure 7. Voltage doubler circuit.

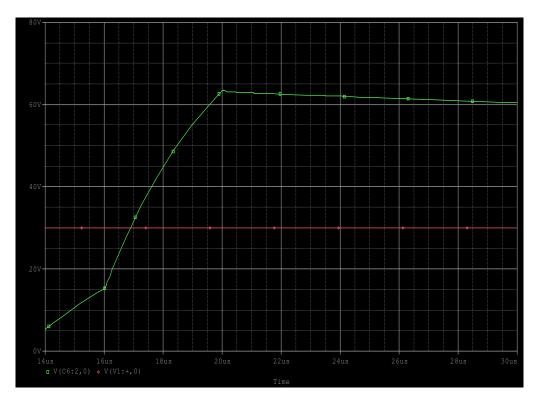


Figure 8. Output voltage waveform of voltage doubler.

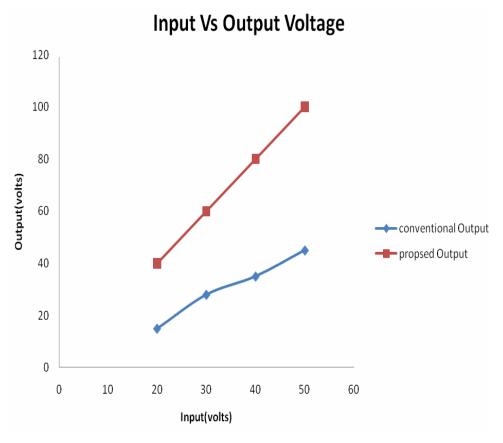


Figure 9. Performance comparison between conventional and proposed method.

high switching frequencies.

#### Conclusion

A high step-up DC-DC converter was proposed for fuel cell system. The proposed converter comprised of an input-current doubler, an output-voltage doubler, and an active-clamp circuit. The input-current doubler and the output-voltage doubler achieved a much higher voltage conversion ratio by using a low turns ratio transformer. Thus, overall efficiency of the system has been developed by reduced turns ratio transformer, since it attenuates the leakage flux losses while transferring the energy. Reverse recovery problem of the rectifying diodes was removed by a series resonant circuit of the output-voltage doubler in the way of producing the counter emf. The active-clamp circuit clamped the surge voltage of switches and recycled the energy stored in the leakage inductance of the transformer. Simulation results proved that based on the transformer design, better performance of the converter can be achieved for any value of input voltage.

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